Please amend the Abstract on page 20, lines 2-23, as follows:

A one-chip system large-scale integrated circuit ~~comprises:~~ includes a storage circuit in which a program has been stored; a processor circuit for processing an operation in accordance with the program using a program counter, a computing unit and a register; and a peripheral circuit, capable of sending and receiving a signal to and from the processor circuit using at least one functional block, for carrying out a predetermined logical operation in accordance with an input signal. The processor circuit ~~further comprises:~~ also includes a selection element for optionally selecting one of ~~the~~ a plurality of outputs ~~of the program counter, the computing unit and the register, at least one output of the storage circuit, and one of the outputs of a plurality of internal signals in the peripheral circuit including the output of the functional block;~~ and a monitor control element for controlling a selection of a result signal from ~~any~~ an operation process ~~in any place of the processor circuit, the storage circuit and the peripheral circuit, on the basis of~~ based on an external monitor signal ~~which is supplied from the outside of the system LSI via an external terminal~~. Thus, when the debug of the one-chip system LSI is monitored, a region and operation process having caused a bug can be precisely monitored, so that it is possible to carry out an efficient debug.